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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: JIAN ZHU, ET AL.)
Application Serial No.: 10/082,450) Attorney Docket No.: IT0085-US
Filed: February 23, 2002)
Title: OPTIMIZING SWITCHING ELEMENT)
FOR MINIMAL LATENCY)

**SUBMISSION OF REVOCATION OF POWER OF ATTORNEY
AND GRANT OF POWER OF ATTORNEY**

Assistant Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

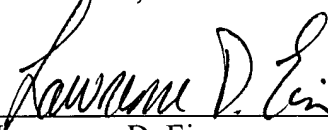
Applicants hereby submit the attached Revocation of Power of Attorney and Grant of Power of Attorney in the above-identified application. Should there be any questions with respect to this submission a representative of the Patent Office is requested to contact the undersigned.

Respectfully submitted,

JIAN ZHU, ET AL.

Date: October 8, 2004

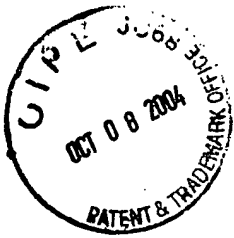
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Customer No. 28970



PATENT
Customer No. 28970

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Patent Application and Patent Numbers: See attached "Schedule A"

REVOCATION OF POWER OF ATTORNEY
AND GRANT OF NEW POWER OF ATTORNEY

The undersigned, a representative authorized to sign on behalf of the assignee owning all of the interest in the listed and pending patent applications and issued patents on the attached sheet (Schedule A), hereby revokes all previous powers of attorney or authorization of agent granted in these patents before the date of execution hereof. The undersigned verifies that **INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE** is the assignee of the entire right, title, and interest in each of the pending patent applications and issued patents listed on the attached Schedule A and is identified as the assignee by assignments from the inventor(s) in the listed pending patent applications and issued patents as filed accordingly at the U.S. Patent and Trademark Office. The undersigned certifies that the evidentiary documents have been reviewed and to the best of the undersigned's knowledge and belief, title in each of the pending patent applications and issued patents listed on the attached Schedule A is in the assignee **INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE**.

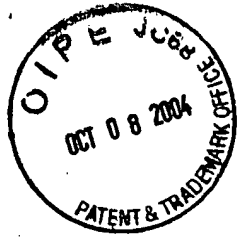
By the undersigned's signature, **INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE** hereby grants power of attorney for each of the pending patent applications and issued patents listed on the attached Schedule A to **SHAW PITTMAN LLP**, Michael D. Bednarek, Reg. No. 32,329; Lawrence J. Gotts, Reg. No. 31,163; Aslan Baghdadi, Reg. No. 34,542; Yitai Hu, Reg. No. 40,653; Elizabeth M. Roesel, Reg. No. 34,878; David C. Isaacson, Reg. No. 38,500; Steven P. Arnheim, Reg. No. 43, 475; Poh C. Chua, Reg. No. 44,615; Michael A. Oblon, Reg. No. 42,956; Lawrence D. Eisen, Reg. No. 41,009; Mark Koehn, Reg. No. 46,271; Michelle S. Marks, Reg. No. 41,971; Brett C. Martin, Reg. No. 52,000; Chad D. Wells, Reg. No. 50,875; Tara L. Hutchings, Reg. No. 46,559; John Kasha, Reg. No. 53,100; Ann P. McGeehan, Reg. No. 45,839; June E. Cohan, Reg. No. 43,741; and Joanne Kim, Reg. No. 51,193, both jointly and separately as its attorneys with full power of substitution and revocation, to transact all business in the Patent and Trademark Office connected with each of the pending patent applications and issued patents listed on the attached Schedule A.

Please send all future correspondence concerning the pending patent applications and issued patents listed on the attached Schedule A to SHAW PITTMAN LLP at the following address:

Shaw Pittman LLP
1650 Tysons Blvd.
McLean, Virginia 22102

Dated: Sept. 9, 2004

By: Benjamin Wang
(name) Benjamin Wang
(title) Deputy General Director
Industrial Technology Research Institute



Schedule A

Filing Date	Title	Inventors	Application Number	Publication Number	Patent Number
11-20-00	RUNNING-SUM ADDER NETWORKS DETERMINED BY RECURSIVE CONSTRUCTION OF MULTI-STAGE NETWORKS	Shuo-Yen Robert Li (李碩彥)	09/716,574		6,591,285
06-15-01	CONDITIONALLY NONBLOCKING SWITCH OF THE COMPRESSOR TYPE	Shuo-Yen Robert Li (李碩彥)	09/882,104	US 2002-0031121 A1	
06-15-01	CONFIGURING EQUIVALENT MULTI-STAGE INTERCONNECTION NETWORKS IN THE BIT-PERMUTING STYLE	Shuo-Yen Robert Li (李碩彥) Lu Wa Chiang (蔣路樺)	09/882,113	US 2002-0031118 A1	
06-15-01	SWITCHING CONCENTRATOR	Shuo-Yen Robert Li (李碩彥)	09/882,439	US 2001-0055304 A1	
06-15-01	SELF-ROUTING DEVICE FOR SWITCHING PACKETS OF DIFFERENT LENGTHS	Shuo-Yen Robert Li (李碩彥)	09/882,317	US 2001-0053157 A1	
06-15-01	PACKET SWITCH WITH ONE-STOP BUFFER IN MEMORY WITH MASSIVE PARALLEL ACCESS	Shuo-Yen Robert Li (李碩彥) Jian Zhu (朱健)	09/882,760	US 2001-0055304 A1	
06-15-01	CONDITIONALLY NONBLOCKING SWITCH OF THE DECOMPRESSOR TYPE	Shuo-Yen Robert Li (李碩彥)	09/882,133	US 2003-0063602 A1	
06-15-01	CONDITIONALLY NONBLOCKING SWITCH OF THE EXPANDER TYPE	Shuo-Yen Robert Li (李碩彥)	09/882,087	US 2002-0039363 A1	
06-15-01	CONDITIONALLY NONBLOCKING SWITCH OF THE UPTURNED COMPRESSOR TYPE	Shuo-Yen Robert Li (李碩彥)	09/882,761	US 2002-0094151 A1	
06-15-01	CONDITIONALLY NONBLOCKING SWITCH OF THE UPTURNED	Shuo-Yen Robert Li (李碩彥)	09/881,827	US 2002-0031117 A1	

Filing Date	Title	Inventors	Application Number	Publication Number	Patent Number
	DECOMPRESSOR TYPE				
06-15-01	CONDITIONALLY NONBLOCKING SWITCH OF THE UPTURNED EXPANDER TYPE	Shuo-Yen Robert Li (李碩彥)	09/882,129	US 2002-0031110 A1	
06-15-01	CONDITIONALLY NONBLOCKING SWITCH OF THE CIRCULAR-UNIMODAL TYPE	Shuo-Yen Robert Li (李碩彥)	09/882,099	US 2002-0037005 A1	
06-15-01	CONDITIONALLY NONBLOCKING SWITCH OF THE UNIMODAL-CIRCULAR TYPE	Shuo-Yen Robert Li (李碩彥)	09/882,202	US 2002-0027904 A1	6,657,998
06-15-01	CONDITIONALLY NONBLOCKING SWITCH OF THE CIRCULAR EXPANDER TYPE	Shuo-Yen Robert Li (李碩彥)	09/882,139	US 2003-0086420 A1	
06-15-01	GENERALIZED DIVIDE-AND-CONQUER NETWORKS	Shuo-Yen Robert Li (李碩彥)	09/882,423	US 2002-0097714 A1	
06-15-01	GENERAL SELF-ROUTING CONTROL MECHANISM OVER BIT-PERMUTING SWITCHING NETWORKS	Shuo-Yen Robert Li (李碩彥)	09/882,005	US 2002-0051447 A1	
06-15-01	SELF-ROUTING CONTROL MECHANISM OVER MULTISTAGE INTERCONNECTION NETWORKS OF CONCENTRATORS	Shuo-Yen Robert Li (李碩彥)	09/882,328	US 2002-0118677 A1	
06-15-01	MULTICAST CONCENTRATORS	Shuo-Yen Robert Li (李碩彥)	09/882,413	US 2003-0053456 A1	
06-15-01	GENERAL SELF-ROUTING MECHANISM FOR MULTICASTING CONTROL OVER BIT-PERMUTING SWITCHING NETWORKS	Shuo-Yen Robert Li (李碩彥)	09/882,075	US 2002-0024951 A1	
06-15-01	PHYSICAL IMPLEMENTATION OF SWITCHING FABRICS CONSTRUCTED FROM RECURSIVE	Shuo-Yen Robert Li (李碩彥)	09/882,410	US 2002-0093950 A1	

Filing Date	Title	Inventors	Application Number	Publication Number	Patent Number
	2-STAGE INTERCONNECTION				
06-15-01	ROUTING SCHEMES FOR PACKET SWITCHING NETWORKS	Shuo-Yen Robert Li (李碩彥) Jian Zhu (朱鍵) Chu Man Lau (劉主民) Wan Lam (林雲)	09/881,870	US 2004-0066799 A1	
06-15-01	MULTISTAGE INTERCONNECTION NETWORKS OF MULTICAST CONCENTRATORS WITH SELF-ROUTING CONTROL MECHANISM	Shuo-Yen Robert Li (李碩彥)	09/882,112	US 2002-0031124 A1	
02-23-02	SCALABLE 2-STAGE INTERCONNECTIONS	Jian Zhu (朱鍵) Shuo-Yen Robert Li (李碩彥) Lu Wa Chiang (蔣路樺)	10/082,449	US 2003-0112797 A1	
02-23-02	OPTIMIZING SWITCHING ELEMENT FOR MINIMAL LATENCY	Jian Zhu (朱鍵) Shuo-Yen Robert Li (李碩彥)	10/082,450	US 2003-0118054 A1	